

Amendments to the Claims

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1.-21. (Canceled).

22. (Currently amended) ~~In a computer system having a host processor under control of an operating system and a basic input/output system, the host processor having an operating state and at least one low power state, an input/output device providing input signals to the computer system, and a microcontroller on the input/output device, a~~ method comprising:

detecting a command to transition ~~the a~~ host processor to a low power state;

writing a first sleep type to a first register to specify the low power state for the host processor;

notifying ~~the a~~ microcontroller of the transition command to the host processor;

pointing the host processor to an instruction that, when executed, causes the host processor to write a second sleep type to a second register that controls the power state of the microcontroller, wherein said second sleep type is not recognized by the microcontroller; and

changing the power state of the microcontroller.

23. (Currently amended) The method of claim 22, wherein ~~the step of~~ changing the power state of the microcontroller comprises changing the microcontroller to a like power state to that of the host processor.

24. (Currently amended) The method of claim 22, wherein ~~the step of~~ changing the state of the microcontroller comprises shutting down the microcontroller.

25. (Original) The method of claim 22, wherein the computer system has at least one peripheral device, and further including notifying the peripheral device to perform a custodial function.

26. (Currently amended) ~~In a computer system having a host processor under control of an operating system and a basic input/output system, the host processor having an operating state and at least one low power state, an input/output device providing input signals to the computer system, and a microcontroller on the device, a~~ A memory device containing code causing that is executable in the computer system and causes the computer system to perform the following acts:

detecting a command to transition the a host processor to a low power state;

write a first sleep type to a first register to specify the low power state for the host processor;

notifying the a microcontroller of the transition command to the host processor;

point the host processor to an instruction that, when executed, causes the host processor to write a second sleep type to a second register that controls the power state of the microcontroller, wherein said second sleep type is not recognized by the microcontroller; and

changeing the power state of the microcontroller.

27. (Original) The memory device of claim 26, wherein the memory device code causes the computer system to perform the act of changing the microcontroller to a like power state to the host processor.

28. (Original) The memory device of claim 26, wherein the memory device code causes the computer system to perform the act of shutting down the microcontroller.

29. (Original) The memory device of claim 26, wherein the computer system includes at least one peripheral device, and wherein the memory device causes the computer system to perform the act of notifying the peripheral device to perform a custodial function.

30. (New) The method of claim 22 further comprising transitioning out of the low power state and executing said instruction.

31. (New) The memory device of claim 26 wherein the memory device code causes the computer system to perform the acts of transitioning out of the low power state and executing said instruction.